

MODELLING AND SIMULATION OF PV CLOSED LOOP SYSTEM TO REDUCE NONLINEARITIES WITH DECREASE IN CAPACITOR SIZE USING MATLAB.

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Abstract—

Renewable energy growth leads to green power, it has witnessed its growth over past decade. A photovoltaic regulation is very necessary to obtain maximum power. This paper estimates the dynamic resistance for every change in irradiance. An adaptive voltage control method is used by modeling the systems such that the feedback voltage is being tracked with the MPPT voltage for every change in irradiance, this will improve the performance of the controller by reducing the rise time. Thus the MPPT controller period is reduced minimizing the system error using a proposed controller method.

Keywords— Adaptive voltage control, photovoltaic power systems, signal modelling, dynamic resistance, Maximum Power Point Tracking (MPPT).

I. INTRODUCTION

Renewable energy sources has led to new research technology which aims to provide a good scope to emerge the growth and consumption of green power minimizing the impact of global warming. PV cells are used to extract their irradiance from the sun and to convert into electrical energy. But the operating point of a photovoltaic (PV) cells changes with change in irradiance so a proper control algorithm is necessary to obtain maximum power by shifting the operating point of PV cell. Several MPPT algorithms such as P&O method, incremental conductance method, and direct method have been used. Theselection of algorithm is done based on the converging time, response. The PV array power and current characteristics are highly nonlinear and are affected by irradiance and temperature variation. Therefore a maximum power point tracking (MPPT) is required to handle such problems and ensure that PV system is operating at MPP. PV systems suffer from three main problems

- High fabrication cost
- Low conversion efficiency when variable weather condition.
- Nonlinearity between PV array output power and current.

Among the MPPT methods, hill climbing/perturb and observe finds its advantages in

- Simplicity
- Ease implementation
- Low cost

Hill climbing operates by perturbing the system by changing the converter power duty cycle and to observe its effect on output array power. However it has three major drawbacks.

- Slow converging to optimum operating point.
- At steady state condition the amplitude of PV power oscillates around the maximum point that causes system power losses.
- During cloudy days irradiance varies quickly operating point moves away from maximum optimum point.

The output reference of these algorithms can be PV voltage [1]-[3], the PV current or just duty cycle of dc/dc converter. The best option is to control the PV voltage since it changes slowly and more stable. Controlling the PV current may result in voltage drop during transients and controlling the duty cycle leads to an inappropriate control of PV voltage and current as well as more losses and stress in

the converter. The value of PV voltage is continuously updated by MPPT or LP Tracker. In MPPT operation the PV voltage is very close to MPP voltage, in LPPT operation the PV voltage varies from MPP to open circuit voltage. Thus a fast and stable regulation in the entire operating range is required for proper tracking. The influence of dynamic resistance on PV voltage control for a buck converter has been studied. The authors analyze the single and cascaded feedback loops. It is shown that system can become unstable if a correct regulation and capacitor sizing is not carried out. This paper analyzes the influence of dynamic resistance for dc/dc step up stage single phase PV converter. The paper proposes an adaptive control in order to obtain a PV voltage regulation. The dynamic resistance estimation is done from measured variables of the converter, namely PV voltage and inductor current and the controller is continuously updated making use of that estimation.

II. SYSTEM MODELLING

The system to be analyzed is shown in Fig. 1. It mainly consists of a PV array, an input capacitor C, a boost converter, a bus capacitor C_{bus}, and a single phase inverter. This paper only able to perform cascaded regulation of step up converter, which will consist of outer PV voltage loop and an inner inductor current loop.

A. Inductor current control.

In some conditions if the system has to operate under low power point tracking (LPPT), with low inductor current, the control should be able to overcome both continuous conduction mode (CCM) and discontinuous conduction mode (DCM). So for that purpose a current control is used based on technique proposed in [6].

Making use of Fig 1 applying Kirchhoff's voltage law we have

$$V_{pv} - V_{out} = L \cdot di_L/dt \tag{1}$$

$$V_{pv} - (1-d) \cdot V_{bus} = L \cdot di_L/dt \tag{2}$$

Where d is the duty cycle.

Dividing (2) by V_{bus} and reorganizing the equation we have

$$d - (1 - V_{pv}/V_{bus}) = d - d_{ccm} = L \cdot di_L/dt \cdot 1/V_{bus} \tag{3}$$

where d_{ccm} is defined as

$$d_{ccm} = 1 - V_{pv}/V_{bus} \tag{4}$$

The value of d_{ccm} can be estimated from voltages V_{pv} and V_{bus} which are measured variables. The value obtained d_{ccm,ff} is used as feed forward compensation. Making use of (3) and (4) this feed forward compensation on the loop for inductor current regulation in CCM is shown in Fig. 2 where C_i represent the controller, S_i current digital sampler and H_i inductor current sensing. A bus voltage compensation is also added for bus measurement V_{bus,ff}. Making use of a lead lag compensator where T_s is the current sample time, the digital sample r_{S_i} can be approximated as [4]

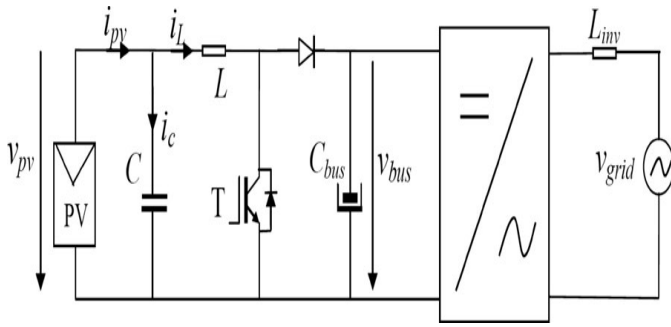


Fig: 1.PV singlephase conversionsystem.

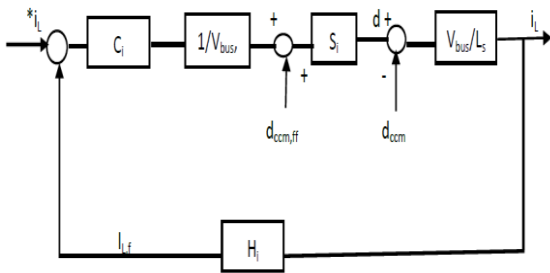


Fig: 2. Inductorcurrentcontrolloop for CCM

$$S_i(s) = 1 / (1.5 \cdot T_{si} \cdot s + 1) \tag{5}$$

The inductor currentsensing transferfunction H_i can be expressed as $H_i(s) = K_i / (T_i \cdot s + 1)$ (6)

Where T_i is the time constant of inductor currentsensing and K_i is the scaling constant of inductor currentsensing whose value is taken as 1 for simplicity.

The presence of a sampler and due to the effect of the measurement retardation on V_{pv} and V_{bus} , the forward duty cycled $d_{ccm,ff}$ doesn't eliminate the effect of PV generator for high frequency. A simple PI controller is used to obtain regulation independent of dynamic resistance.

B. Conventional Modeling of PV Voltage Control Loop.

From Fig. 1 and Kirchhoff's current law

$$i_{pv} = i_c + i_L \tag{7}$$

By replacing the capacitor current in (7)

$$i_{pv} = C \cdot dV_{pv}/dt + i_L \tag{8}$$

Applying Laplace transform to (8) we obtain the current to voltage transfer function as

$$V_{pv}(s) / (-i_L(s) + i_{pv}(s)) = 1 / (C \cdot s) \tag{9}$$

Making use of (9), the loop for the PV voltage v_{pv} regulation is shown in Fig. 3, where C_v represents the controller, S_v the voltage digital sampler, H_v the PV voltage sensing, G_{icl} the current closed-loop, g the irradiance, and T the array temperature. It can be observed in the Fig. 3, that process is influenced by the PV current i_{pv} . Being T_{sv} , the voltage sample time, the digital sampler S_v can be approximated as $S_v(s) = 1 / (1.5 \cdot T_{sv} \cdot s + 1)$ (10)

The PV voltage sensing transfer function H_v can be expressed as $H_v(s) = K_v / (T_v \cdot s + 1)$ (11)

Where T_v is the time constant of PV voltage sensing and K_v is the scaling constant of PV voltage sensing which will be taken as 1 for simplicity of analysis. If the inner current and outer voltage loops are totally decoupled, the inner closed-loop could be modeled as 1. Although the loops are decoupled in this paper, the inner closed-loop will be modeled as a first order for more precision. Being ω_c the angular cutoff frequency of the current control, their transfer function is given by

$$G_{icl}(s) = 1 / (s / \omega_c + 1) \tag{12}$$

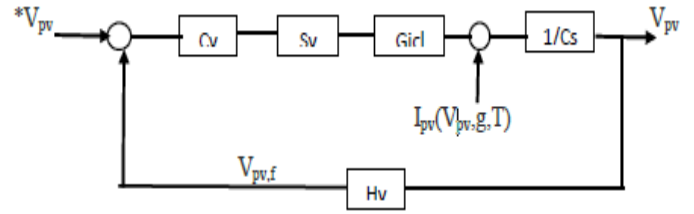


Fig: 3. Conventional PV voltage control loop.

C. PV cell array modeling.

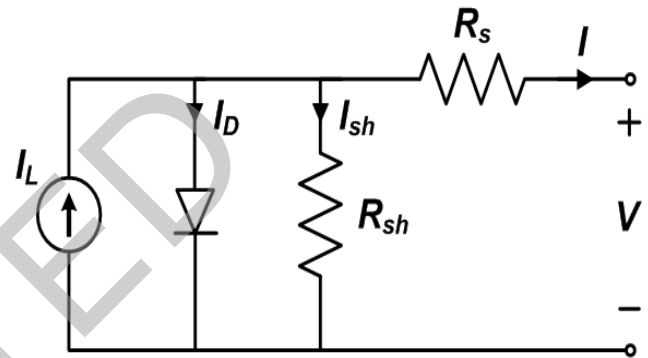


Fig: 4. PV cell equivalent circuit.

The Fig. 4 shows the equivalent circuit of PV cell as $I_{pv} = I_{ph} - I_o \cdot \exp[(V_{pv} + R_s \cdot i_{pv}) / V_t - 1] - (V_{pv} + R_s \cdot i_{pv}) / R_p$ (13)

Where I_{ph} and I_o are photogenerated and saturation current of array, R_s is the equivalent series resistance, R_p is the equivalent shunt resistance and V_t is the terminal voltage given by

$$V_t = N_s \cdot m \cdot (K \cdot T) / q \tag{14}$$

Where N_s is the cells connected in series, m is the ideality factor of the diode, k is the Boltzmann constant, T is the generator temperature and q is the electron charge. To obtain a linear equation for PV current I_{pv} as a function of PV voltage V_{pv} and irradiance g , the variation of current with temperature is not considered since it changes very slowly and being easily compensated by regulator. The dynamic resistance of PV array R_{pv} which will represent the voltage influence is calculated using (13) as

$$R_{pv} = - V_{pv} / i_{pv} = R_d // R_p + R_s \tag{15}$$

III. CONVENTIONAL CONTROL

In this section the variation of regulation performance is observed for boost stage of a typical PV converter whose features are shown in Table 1. The specific operation of the PV array used for analysis is shown in Table 2. The inductor current can be controlled by model developed in section 2A. For the PV voltage regulation a PI controller

is used in the converter. In the conventional method from Fig. 3, the rise time of the voltage responses should be constant and close to 9.5ms. But the rise time is much higher and it is highly dependent on voltage level. So in conventional systems there is no overshoot as the phase margin is high. So use of small capacitor so the regulation becomes poor such as because of MPPT Controller periodismore. And no rejection of irradiance, soon must use a new technique so

that controller time is reduced and thus increasing the system stability, thereby estimating the change in resistance for a change in interval of time and irradiance (g). gives rise to implementation of proposed controller.

TABLE I

Features of DC/DC boost stage commercial PV Converter.

| | |
|--|--------------|
| Nominal power | 5000W |
| Input capacitor C | 40µF |
| Boost inductor L | 750µH |
| Commutation frequency f_c | 16KHz |
| PV voltage sample time T_{sv} | 250µs |
| Inductor current sample time T_{si} | 125µs |
| Time constant of the PV voltage sensing | 74µs |
| Angular cut off frequency of the current control ω_{ic} | 2π.450rad/s. |
| Bus voltage V_{bus} | 350V |

TABLE II

Specifications of PV array formed by four string of 12 BP585 modules.

| | |
|---|---------|
| Nominal power | 4080 W |
| MPP voltage V_{mpp} | 216 V |
| MPP current I_{mpp} | 18.9 A |
| Open-circuit voltage V_{oc} | 264 V |
| Short-circuit current I_{sc} | 20 A |
| Equivalent series resistance R_s | 0.848 Ω |
| Equivalent shunt resistance R_p | 736 Ω |
| Cells connected in series in a module N_s | 36 |
| Ideality factor m | 1 |

IV. PROPOSED CONTROL.

To solve the problems of the dynamic resistance variation, an adaptive control is proposed in this paper. For each voltage sample time T_{sv} , the control time at the dynamic resistance and use this value in order to adapt one parameter of the controller according to the dynamic resistance variation. In this way, the nonlinear system variability is compensated.

A. Dynamic Resistance Estimation

It can be seen that i_{pv} variation is caused by both v_{pv} and g variations. The irradiance variation is unknown but can have important effect on the current during a cloud passage. This fact hinders the estimation of dynamic resistance which represents current variation caused by voltage variation. For this reason 100Hz ripple is used. In real system the irradiance component around 100Hz is negligible. As a result evolution of 100Hz ripple of i_{pv} is used by 100Hz ripple of v_{pv} . It follows that

$$I_{pv100} = K_g \cdot g_{100} - V_{pv100} / R_{pv} = -V_{pv100} / R_{pv} \tag{16}$$

Where i_{pv100} , g_{100} and V_{pv100} are respectively the PV current, irradiance and PV voltage obtained after applying a type I Chebyshev 100Hz bandpass filter (100Hz BPF) to the original signal.

Now, an estimation of the dynamic resistance, $R_{pv,est}$ can be easily found by dividing the rms value of v_{pv100} (V_{pv100}) by the rms value of

$$R_{pv100} = V_{pv100} / I_{pv100} \tag{17}$$

But generally the measured current is i_L instead of i_{pv} . However i_{pv100} can be worked out by means of 100Hz ripple of i_L (i_{L100}) and V_{pv100} as follows:

$$I_{pv100} = i_{L100} + C \cdot V_{pv100} (K) - V_{pv100} (K-1) / T_{sv} \tag{18}$$

B. Controller Design

The proposed controller is a second order one with two constant parameters K_p and T_n , and one variable parameter T_m . $C_v(s) = K_p \cdot (T_n \cdot s + 1) / (T_m \cdot s + 1 / T_m \cdot s)$

So from (19) it is observed that, once the dynamic resistance is known, the variability of the system can be eliminated by controller parameter T_m by forming

$$T_m = C \cdot R_{pv,est} \tag{20}$$

The below fig 5 shows about proposed controller block

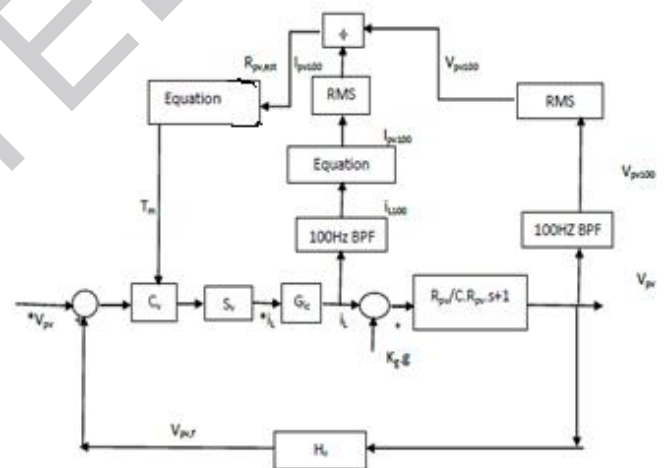


Fig: 5. Proposed controller.

C. MATLAB SIMULATION AND RESULTS.

This simulation is carried out by using Matlab Simulink. The Fig: 1 is used to analyze the components which play a role to obtain the desired output. A new modeling technique is carried out by modeling the control blocks.

The paper also aims to provide an estimation of dynamic resistance even for a very low frequency of 100Hz means the system is capable of estimating the change in resistance corresponding to the change in voltage values.

An 100Hz BPF filter is provided to estimate the resistance within this band of frequency. The Fig: 6 shows the circuit arrangement using Matlab Simulink.

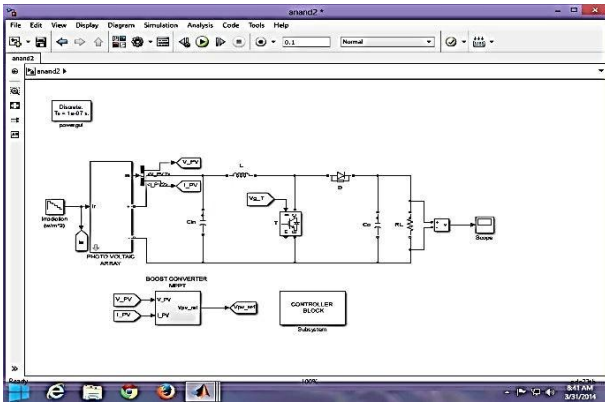


Fig:6 Circuitarrangement usingMatLab.



Fig:7 PVvoltage regulation.

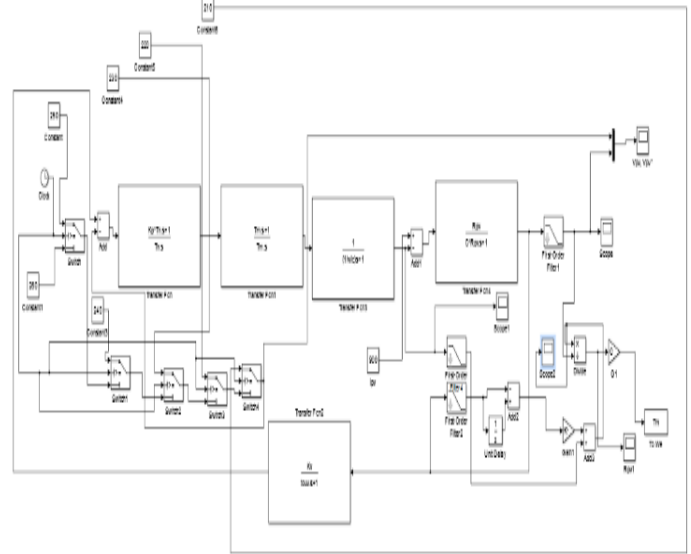


Fig.9 Traditional PV closed loop.

InproposedcontroltheMPPTtimecanbereducedwhichwilllead toimprovementintheperformance.TheMPPTcyclotimecanbereducedto40 ms.FromFig:7.The risetimeofthevoltage response should beconstantandclose to23.9ms.Howeverduringthetransientfrom260Vto250Vthat isnear toopencircuit,therisetimeis18.4ms, whereasfrom220Vto210VaroundtheMPPit is15.1ms.Soinany casewhenusingtheproposed control,therisetimeismuch closeto the design timethanincaseofconventional control.Sobyusingtheproposed control, theMPPTcontrollerperiodcanbe considerablyreducedwhichleadstothe improvementintheperformance.Theproposedcontrollercanreducethetim euptoabout40ms.

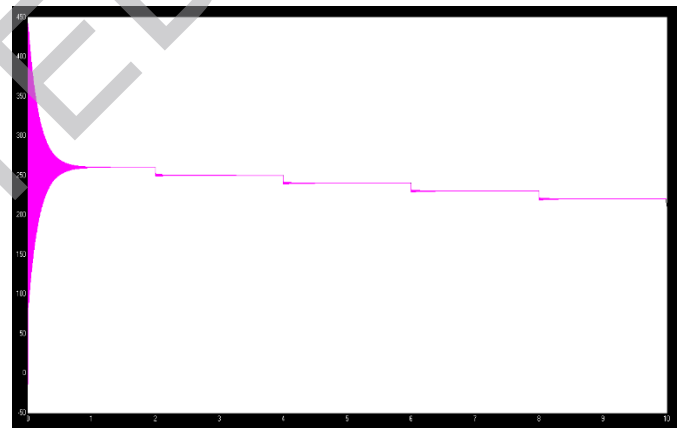


Fig.10 simulation result for reduction in ripple(non linearities)

V.CONCLUSION

The dynamic resistance is firstly estimated from measured variable of the converter. Then the controller is continuously adapted making use of dynamic

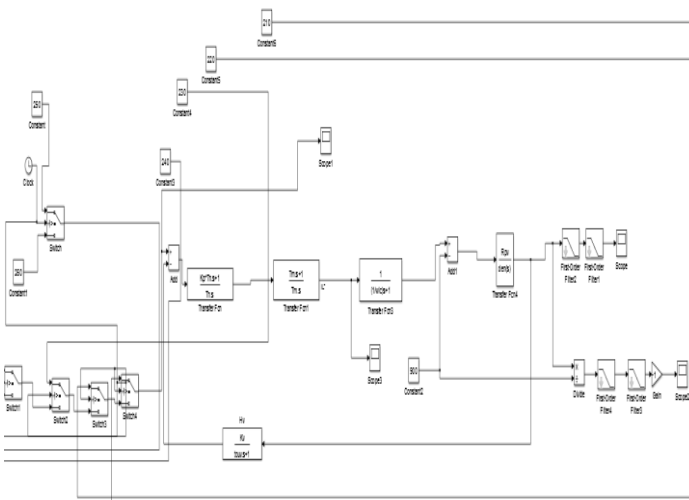
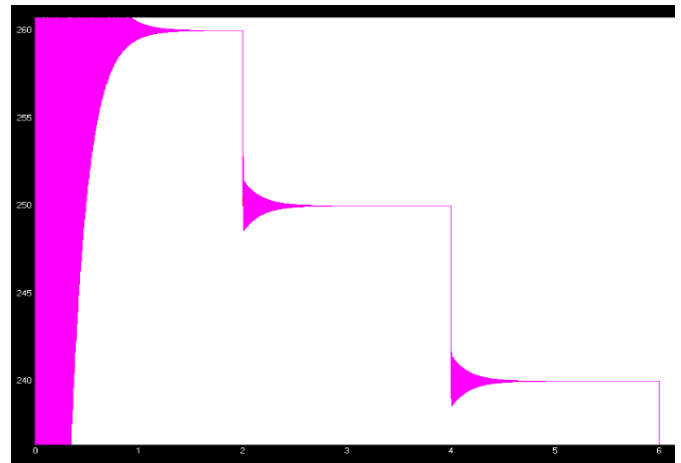


Fig.8 Conventional PV voltage closed loop



micestimation. The resistance is accurately calculated by voltage and current. 100 ripple present in the single phase inverters. This avoids misestimations caused by abrupt irradiance variations. The output voltage is traced with the feed back voltage to have a better voltage regulation. The MPPT controller time is reduced such that the response of the system is increased.

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